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	Filing Date	<b>April 14, 2004</b>
	First Named Inventor	<b>Robert O. Conn</b>
	Examiner Name	<b>Donghee Kang</b>
	Art Unit	<b>2811</b>
	Patent No.	
Attorney Docket Number		<b>X-1322-1-1D US</b>

ENCLOSURES (check all that apply)		
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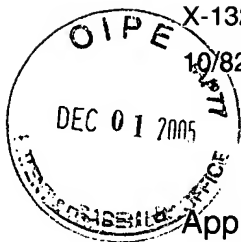
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X-1322-1-D US

10/824,675

PATENT

Conf. No. 8082



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert O. Conn

Assignee: Xilinx, Inc.

Title: Bond and Back Side Etchback Transistor Fabrication Process

Serial No.: 10/824,675

Filing Date: 04/14/2004

Examiner: Donghee Kang

Art Unit: 2811

Docket No.: X-1322-1-1D US

Conf. No.: 8082

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AMENDMENT IN RESPONSE TO THE FINAL OFFICE ACTION

In response to the Final Office Action mailed from the Patent Office on October 6, 2005, Applicant submits the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 5 of this paper.